

Notice of References Cited	Application/Control No. 10/079,925	Applicant(s)/Patent Under Reexamination RAINERI ET AL.	
	Examiner Evan Pert	Art Unit 2829	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-			
	C	US-			
	D	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, SILICON PROCESSING FOR THE VLSI ERA, Volume 2: Process Integration, Lattice Press, 1990, p. 45.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.